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Hajj**

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As the complexity of very large scale integrated (VLSI) current density guidelines and material properties. There is a growing concern that VLSI chip reliability

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Current Calculation on VLSI Signal 3.3 Effective Average Current The reliability of interconnect is the driving point since the current density at this

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Hajj, I.N.; Illinois Univ., system for reliability analysis of VLSI CMOS and computation of average current densities in sections of the power and ground

### **Electromigration failure modes in aluminum**

Electromigration failure modes in aluminum metallization Najm is now with the VLSI Design while the maximum current density in the line

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hajj, i. n. ; li, p. ; \*computer aided design, \*very large scale integration, \*reliability(electronics), \*current density,

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Shaping Interconnect for Uniform Current Density \* Muzhou Shao<sup>1</sup>, in VLSI design, Evolution of VLSI reliability engineering, Proc.

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