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Neil H. E. Weste (born 1951), is an Australian inventor and engineer, Principles of CMOS VLSI Design (initially coauthored with Kamran Eshraghian

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This paper presents a comprehensive survey of existing techniques for interconnect optimization during the VLSI physical design process, with emphasis on recent

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His research interests include CMOS VLSI design, microprocessors, and computer arithmetic. Principles of CMOS VLSI Design Hardcover. Neil H. E. Weste. 12.

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