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Sharma**

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we address these issues in the synthesis of different high performance circuits, namely Domino logic Logic and physical synthesis of high performance circuits.

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the major drawback with the domino dynamic logic circuit is its excessive power dissipation due to the a high performance logic family for GaAs. IEEE Trans

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2.1. High speed domino logic. The circuit topology of the HS domino logic is shown in Fig. 5. At the beginning of the evaluation phase, when clock is high, MP3 turns

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