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VLSI Design: A Novel Approach By Krishna Gopal  
Sharma**

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Abstract. Abstract Dynamic domino logic circuits are widely used in modern digital VLSI circuits. These dynamic circuits are often favoured in high performance

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2.1. High speed domino logic. The circuit topology of the HS domino logic is shown in Fig. 5. At the beginning of the evaluation phase, when clock is high, MP3 turns

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Domino logic circuit is also a kind of Vatajelu, E.I., Peiravi, A., Mahmoodi, H. and Wisland, D.T. (2012) Domino Logic Designs for High-Performance and

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