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Qualification (Astm Special Technical Publication// Stp)
By George A. Brown (Editor) D. C. Gupta (Editor)**

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Patent us8652957 - high-k gate dielectric oxide -

Although the gate dielectric can be formed from materials oxide dielectric materials and a process for making high-integrity gate oxide

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230923 - direct-digital control system for hvac revised 071511.docsection 23 09 23 direct-digital control system for hvac part 1 general 1.1 description

Chapter 3 hafnium-based gate dielectric materials

Hafnium-Based Gate Dielectric Materials importance in the selection process of high-k materials. good integrity of high-k/substrate interface,

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High- dielectric - wikipedia, the free

Replacing the silicon dioxide gate dielectric with another material adds complexity to the manufacturing process.

Strained si/sige mos technology: improving gate

virtual substrates are conventionally 1 3 m thick in order to achieve the desired SiGe material gate dielectric integrity Process-strained Si

D. c. gupta (author of physics for iit-jee) -

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C. (Editor). 2002. Updating Standard Shape Material Properties Database for Design and Reliability Brown, P., M.D. Campbell, R.L. Hawkes, C. Theijsmeijer,

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Special Tooling and Test Equipment C. Carrano, D. Silva, D. Pennington, C. Brown, The tool will not only provide multiple process feedback loops to allow

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Patent us6261934 - dry etch process for

Dry etch process for small-geometry metal gates over thin gate dielectric on new gate materials is a good work Dielectric Gate Stack Process and

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Analytics for us patent no. 6,562,730, barrier in

A barrier layer comprising silicon mixed with an impurity is disclosed for protection of gate dielectrics in integrated transistors. In part . Trigger. Patent Buddy.

Intel's 45nm surprise: high-k dielectrics and

The permittivity of a material, On Intel s 65nm process, the gate oxide is only 1.2nm thick, High-k Dielectric and Metal Gate Transistor Stack.

A00-001

Edited by the Optical Society of America, Washington, D.C be dielectric , conductive Region," ARO Technical Report (1997). [5] D.L. Woolard et. al

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Patent us6770571 - barrier in gate stack for

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are needed to avoid abrupt and costly technological and manufacturing process dielectric gate with a metal new dielectric gate material,

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