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Hardware/Software Co-Design By F. Mayer-Lindenberg**

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Patent us20040212706 - image processing method for

1. A method of image processing an image in a camera module having an image sensor recording an image, said method including the steps of:

Simd processor with programmable counters

Method 500 may be performed by processor 104. a field programmable gate array (FPGA), a digital signal processor (DSP), such as dedicated physical

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Hardware software codesign

Patrick R. Schaumont, "A Practical Introduction to Hardware/Software Codesign" English | 2010 | ISBN: 1441959998 | 396 pages | PDF | 4,2 MB.

Ee382v-ics: system-on-a-chip design

Friedrich Mayer-Lindenberg. Dedicated Digital Processors: Methods in Hardware/Software Co-design John Wiley and Sons, February 2004.

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